

## SIMtrace - Issues

#	Tracker	Status	Priority	Subject	Assignee	Updated	Related issues
1428	Feature	Closed	Normal	increase default trace width in simtrace pcb	tsaitgaist	02/20/2016 06:44 PM	
1429	Feature	Closed	Normal	usb jack type is really hard to manually solder	tsaitgaist	02/20/2016 06:44 PM	
1430	Feature	Closed	Normal	clearly indicate location of 'pin 1' of components and connectors	tsaitgaist	02/20/2016 06:44 PM	
1433	Feature	Closed	Normal	use 90 degree angled jumpers for ERASE and TEST	tsaitgaist	02/20/2016 06:44 PM	
1436	Feature	Closed	Normal	add VCC_PHONE to an unused GPIO	tsaitgaist	02/20/2016 06:42 PM	
1438	Feature	Closed	Normal	add ID-1 slot to SIMtrace	tsaitgaist	02/20/2016 06:42 PM	
1439	Feature	Closed	Low	move ERASE and TEST jumper a tint bit, so a 1x4pin header works	tsaitgaist	02/20/2016 06:42 PM	
1440	Feature	Closed	High	Use through-hole mini-USB-B socket	tsaitgaist	02/20/2016 06:42 PM	
1441	Feature	Closed	High	Use through-hole 2.5mm jack	tsaitgaist	02/20/2016 06:42 PM	
1442	Feature	Closed	High	Make LED footprints 0805 instead of 0603	tsaitgaist	02/20/2016 06:42 PM	
1443	Feature	Closed	High	Adapt to different SPI flash (foot print)	tsaitgaist	02/20/2016 06:42 PM	
1444	Feature	Closed	High	Adapt to different shottky diode footprint	tsaitgaist	02/20/2016 06:41 PM	
1445	Feature	Closed	Low	Some component labels (resistors/capacitors) are not readable	tsaitgaist	02/20/2016 06:41 PM	
1451	Feature	Closed	Normal	Atmel SAM3S to replace SAM7S	tsaitgaist	02/20/2016 06:40 PM	
1453	Feature	Closed	High	Move C21 closer to VCC_SIM pad	tsaitgaist	02/20/2016 06:39 PM	
1454	Feature	Closed	High	add "systocom" text into the copper layer	tsaitgaist	02/20/2016 06:39 PM	
1455	Feature	Closed	High	try to improve trace width / blocking caps	tsaitgaist	02/20/2016 06:39 PM	
1724	Feature	Rejected	Low	extend wireshark dissector for SIM/USIM traces		09/28/2022 08:07 AM	